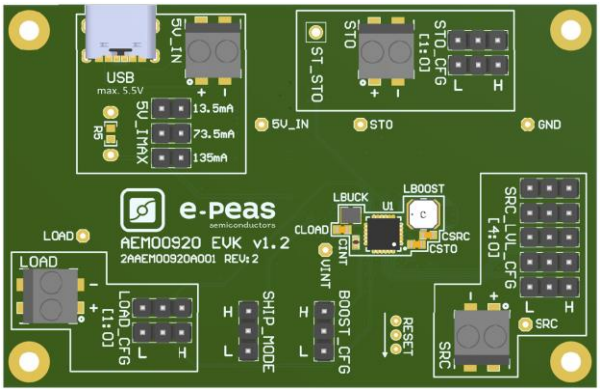
AEM00920
Quick Start Guide EVK



FEATURES

Connectors

- 1 screw connector for the DC source.
- 1 screw connector for the storage element.
- 1 screw connector for the load / application circuit.
- 1 screw connector for the 5 V DC power input.

Configuration

- 5 headers SRC_LVL_CFG[4:0] to configure the SRC constant voltage regulation.
- 2 headers STO_CFG[1:0] to define the storage element protection levels.
- 2 headers LOAD_CFG[1:0] to configure the application circuit regulated voltage.
- 3 headers to configure the 5 V charger maximum current.
- 1 header to configure the boost converter timings.
- 1 header to enable/disable the shipping mode.
- 1 header to enable/disable the custom mode.

Reset

- 3 RESET pads (short each pad to GND from top to bottom to reset the AEM).

Size

- 79mm x 49mm.
- 4 x M2.5 mounting holes.

SUPPORT PCB

BOM around the AEM00920:

	Designator	Description	Quantity	Manufacturer	Part Number
Mandatory	U1	AEM10920	1	e-peas	order at sales@e-peas.com
	CSRC	Ceramic Capacitor 22 µF, 10 V, 20%, X5R 0603	1	Murata	GRM188R61A226ME15D
	LBOOST	Power Inductor 33 µH, 0.68 A, LPS4018	1	Coilcraft	LPS4018-333MRB
	CINT	Ceramic Capacitor 10 µF, 6.3 V, 20%, X5R, 0402	1	Murata	GRM155R60J106ME44D
Optional	CSTO[1]	Ceramic Capacitor 47 µF 6.3 V, 20%, X5R, 0603	1	Murata	GRM188R60J476ME15D
	R_5VIMAX[2]	Resistor (to be defined)	1	To be defined	
	C_5V	Capacitor (to be defined)	1	To be defined	
	R_5V	Resistor (to be defined)	1	To be defined	
	D_5V	Zener diode (to be defined)	1	To be defined	
	LBUCK	Power Inductor 10 µH TDK VLS-CX-1	1	TDK	VLS252012CX-100M-1
	CLOAD	Ceramic Capacitor 22 µF, 10 V, 20%, X5R, 0603	1	Murata	GRM188R61A226ME15D

Footprint & Symbol: information available in the datasheet.

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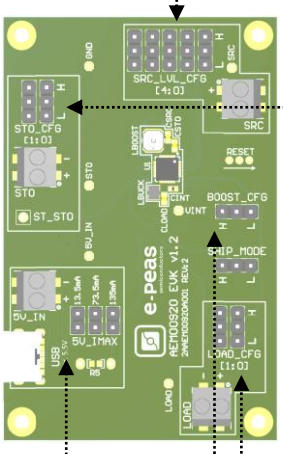
STEP 1: Configure the AEM00920



SRC regulation voltage: SRC_LVL_CFG[4:0] (seen as HIGH if left floating)

Configuration pins					Voltage [V]
SRC_CFG[4:0]					$V_{SRC,REG}$
L	L	L	L	L	0.25
L	L	L	L	H	0.30
L	L	L	H	L	0.35
L	L	L	H	H	0.40
L	L	H	L	L	0.45
L	L	H	L	H	0.50
L	L	H	H	L	0.55
L	L	H	H	H	0.60
L	H	L	L	L	0.65
L	H	L	L	H	0.70
L	H	L	H	L	0.75
L	H	L	H	H	0.80
L	H	H	L	L	0.85
L	H	H	L	H	0.9
L	H	H	H	L	0.95
L	H	H	H	H	1.00

Configuration pins					Voltage [V]
SRC_CFG[4:0]					$V_{SRC,REG}$
H	L	L	L	L	1.10
H	L	L	L	H	1.20
H	L	L	H	L	1.30
H	L	L	H	H	1.40
H	L	H	L	L	1.50
H	L	H	L	H	1.60
H	L	H	H	L	1.70
H	L	H	H	H	1.80
H	H	L	L	L	1.90
H	H	L	L	H	2.00
H	H	L	H	L	2.20
H	H	L	H	H	2.40
H	H	H	L	L	2.60
H	H	H	L	H	2.80
H	H	H	H	L	3.00
H	H	H	H	H	3.20

Storage element threshold voltages: STO_CFG[1:0] (seen as HIGH if left floating)

Configuration pins		Overdischarge voltage [V]	Charge ready voltage [V]	Overcharge voltage [V]	Battery Type
STO_CFG[1:0]		V_{OVDIS}	V_{CHRDY}	V_{OVCH}	
L	L	2.50	2.55	3.80	Lithium-ion Super Capacitor (LiC)
L	H	3.00	3.20	4.12	Lithium-ion battery
H	L	3.00	3.20	4.35	LiPo battery
H	H	3.50	3.55	3.90	Li-ion battery (ultra long life)

Load configuration: LOAD_CFG[1:0] (seen as HIGH if left floating)

Configuration pins		LOAD voltage [V]
LOAD_CFG[1:0]		V_{LOAD}
L	L	OFF
L	H	2.2
H	L	2.5
H	H	2.8

Boost timing configuration: BOOST_CFG (seen as HIGH if left floating)

Configuration pin	Function	
BOOST_CFG	Timing multiplication factor	Minimum L_{BOOST} inductance [µH]
L	x1	4
H	x3	12

5V charger configuration:

Resistor [Ω]	Maximum Charging Current [mA]
R_{5V_IMAX}	$I_{5V,CC}$
370	135.0
680	73.5
1500	33.3
3700	13.5

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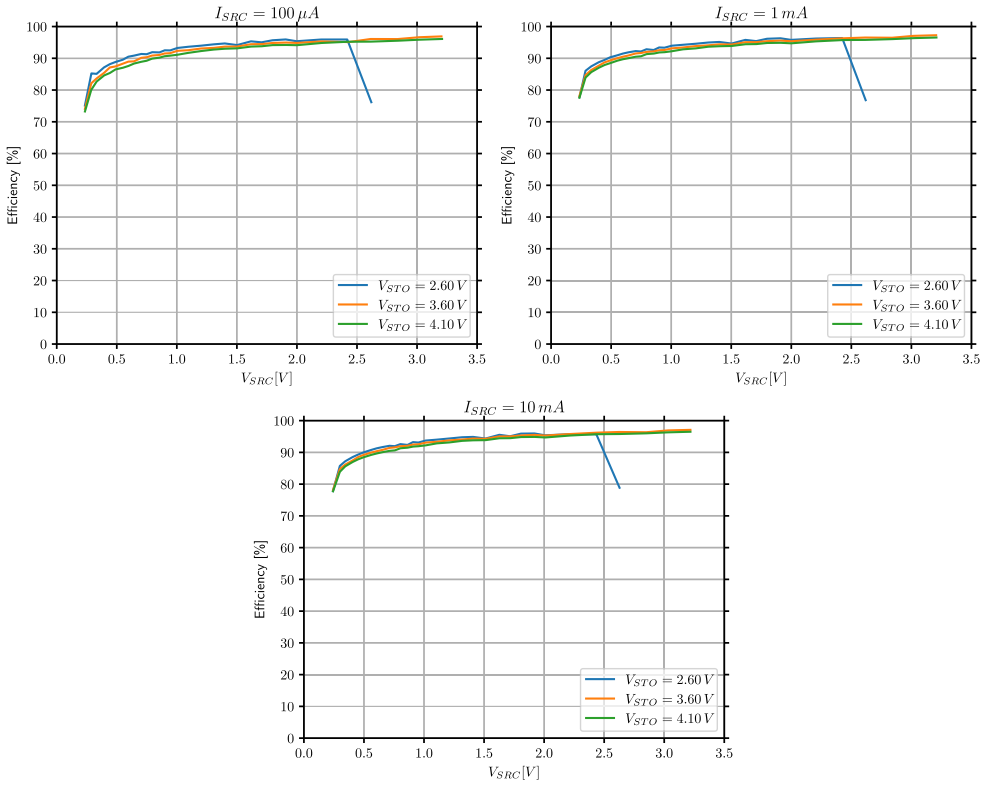
STEP 2: Connect a storage element with a voltage higher than 2.4 V

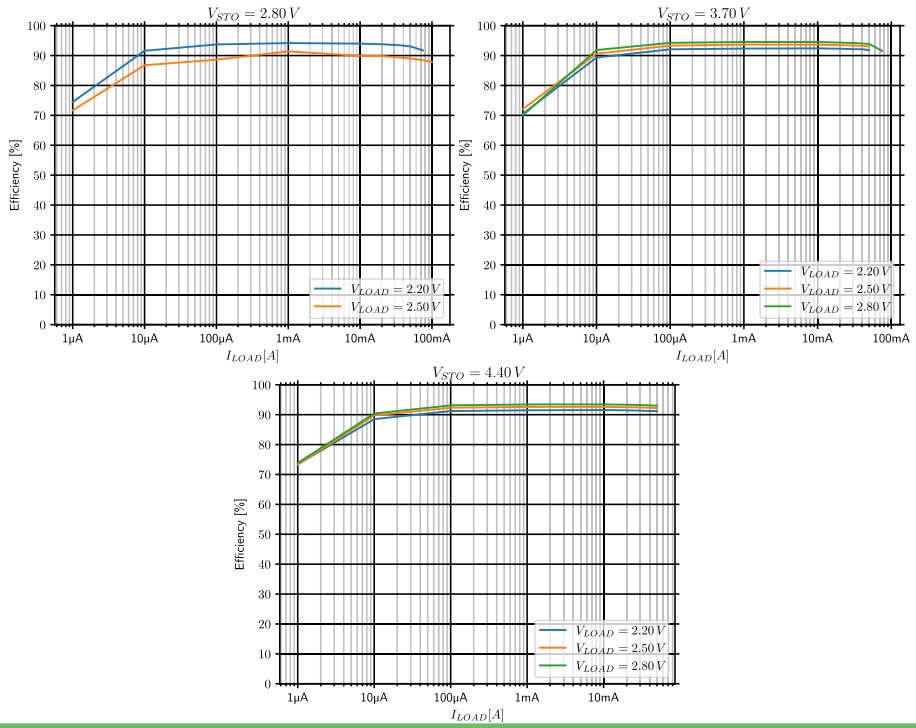
STEP 3: Connect the source or the 5 V power input

STEP 4: Connect the application circuit

- **Boost efficiency** (Lboost = 33 µH Coilcraft LPS4018-333MRB; boost timing x3) :



- **Buck efficiency** (Lbuck = 10 µH TDK VLS252012CX-100M-1) :



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